

## Active

- L1: (140473) (pll (phase near8 loop))
- L2: (126818) (phase near8 (comparator compar\$4))
- L3: (37116) 1 and 2
- L4: (13248) 3 and (phase near8 shift\$3)
- L5: (159560) (width near8 (determin\$4 detect\$3))
- L6: (1212) 4 and 5
- L7: (713) 369/47.28
- L8: (24827) G11B005/09
- L9: (1928) 331/16
- L10: (3802) 331/17
- L11: (10324) H03L007/00
- L13: (8753) H03L007/06
- L12: (1573) 327/159
- L14: (16) 327/159
- L15: (46) 331/16
- L16: (100) 331/17
- L17: (48) 369/47.28

Failed

DBs: USPAT

☐ Plurals

Default operator: OR

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for 10/824264

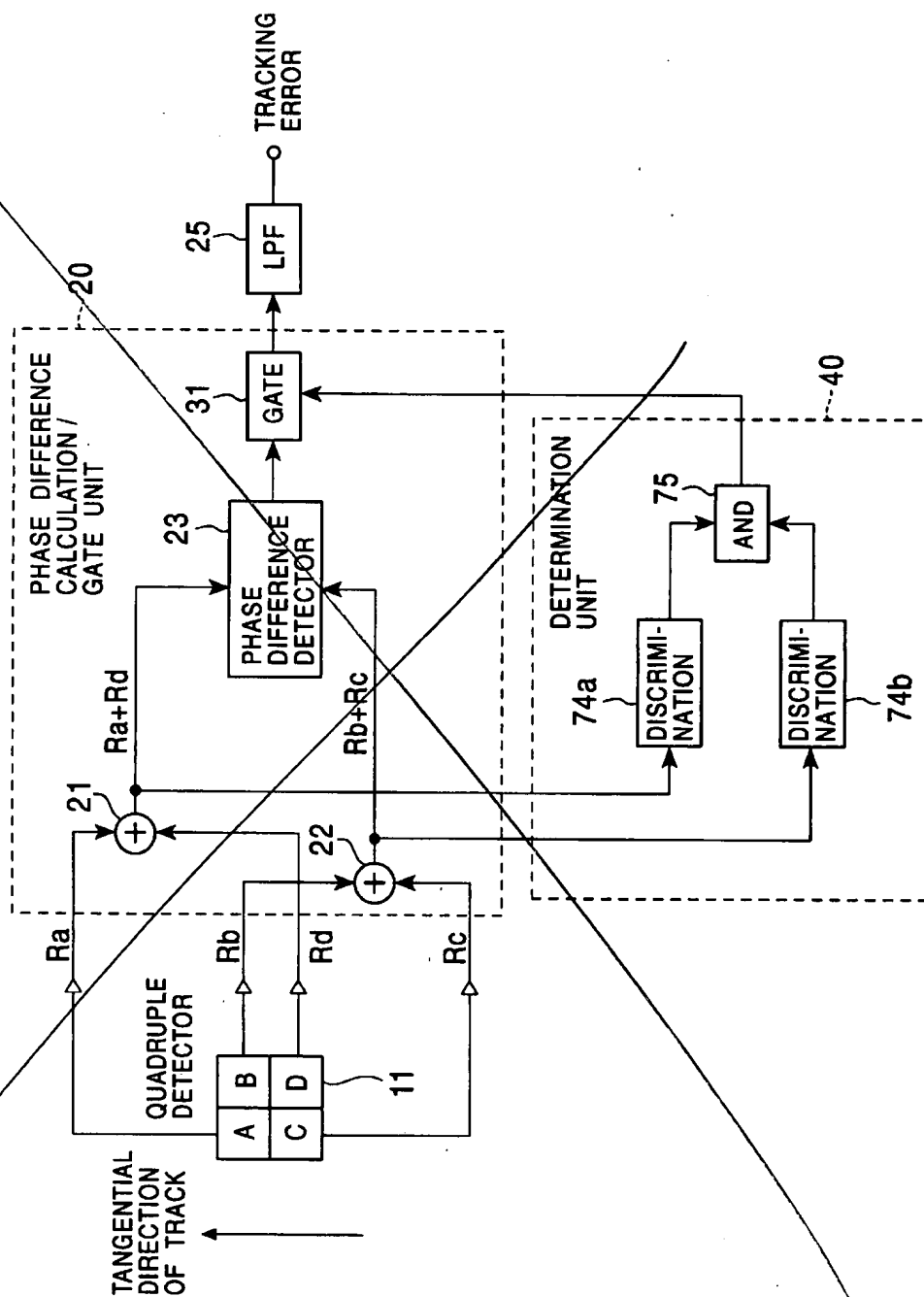
	Type	L #	Hits	Search Text	DBs	Time Stamp	Comment	Error	Definit	Er
1	BRS	L1	140473	(pll (phase near8 loop))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1 6 15:32				
2	BRS	L2	126818	(phase near8 (comparator compar\$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1 6 15:32				
3	BRS	L3	37116	1 and 2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1 6 15:32				
4	BRS	L4	13248	3 and (phase near8 shift\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1 6 15:33				
5	BRS	L5	159560	(width near8 (determin\$4 detect\$3))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1 6 15:33				
6	BRS	L6	1212	4 and 5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1 6 16:07				
7	BRS	L7	713	369/47.28	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1 6 16:07				
8	BRS	L8	24827	G11B005/09	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1 6 16:07				
9	BRS	L9	1928	331/16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1 6 16:07				
10	BRS	L10	3802	331/17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1 6 16:07				
11	BRS	L11	10324	H03L007/00	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1 6 16:08				
12	BRS	L13	8753	H03L007/06	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/1 6 16:08				

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FIG. 12



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Active

L1: (140473) (pll (phase near8 loop))

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L12: (1573) 327/159

L14: (16) 327/159

L15: (46) 331/16

L16: (100) 331/17

L17: (48) 369/47.28

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Default operator: OR

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US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB

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US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB

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Hits

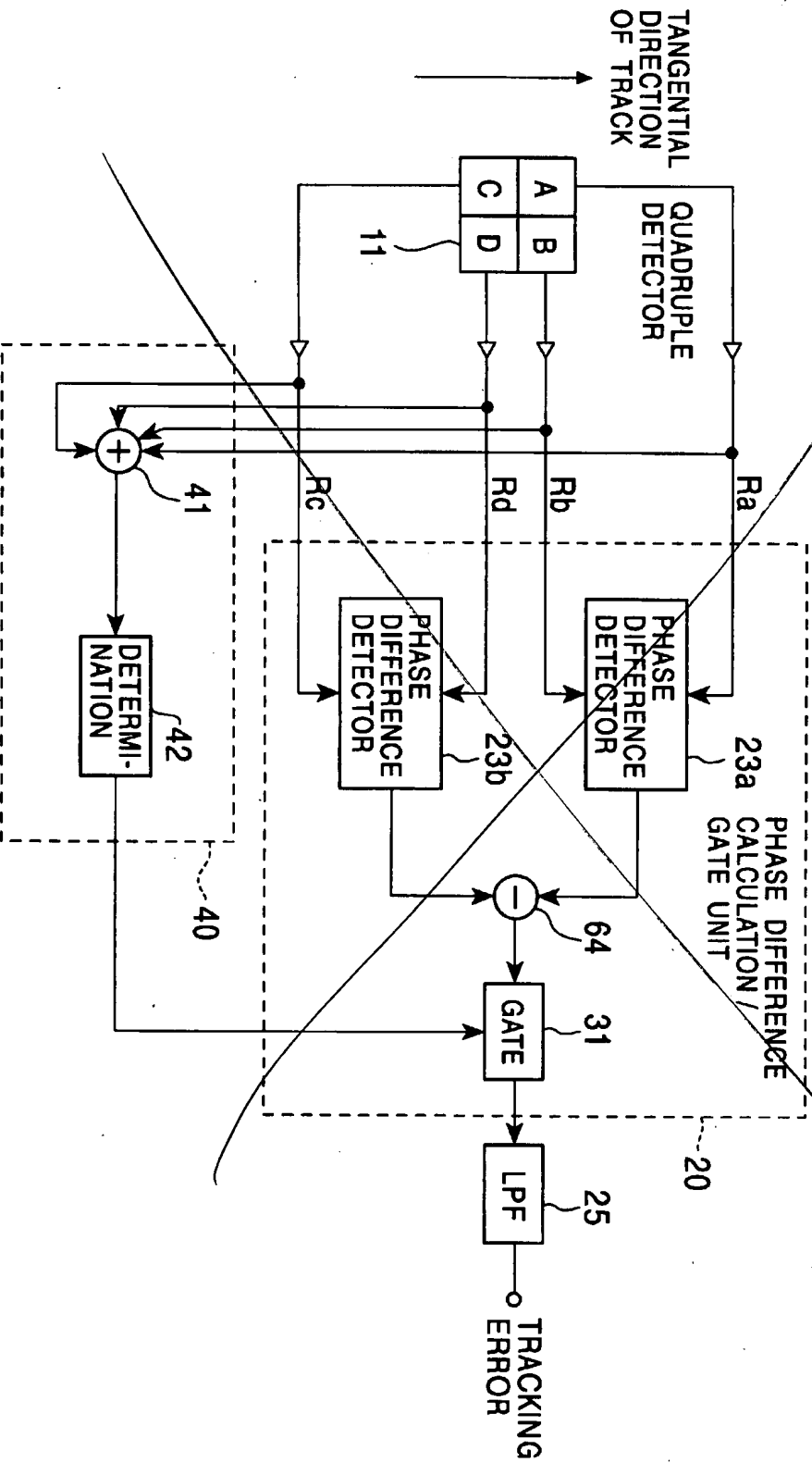
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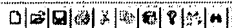
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FIG. 13





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Plurals

QB8 | USPAT

Default operator:

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 BRS form
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	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100	101	102	103	104	105	106	107	108	109	110	111	112	113	114	115	116	117	118	119	120	121	122	123	124	125	126	127	128	129	130	131	132	133	134	135	136	137	138	139	140	141	142	143	144	145	146	147	148	149	150	151	152	153	154	155	156	157	158	159	160	161	162	163	164	165	166	167	168	169	170	171	172	173	174	175	176	177	178	179	180	181	182	183	184	185	186	187	188	189	190	191	192	193	194	195	196	197	198	199	200	201	202	203	204	205	206	207	208	209	210	211	212	213	214	215	216	217	218	219	220	221	222	223	224	225	226	227	228	229	230	231	232	233	234	235	236	237	238	239	240	241	242	243	244	245	246	247	248	249	250	251	252	253	254	255	256	257	258	259	260	261	262	263	264	265	266	267	268	269	270	271	272	273	274	275	276	277	278	279	280	281	282	283	284	285	286	287	288	289	290	291	292	293	294	295	296	297	298	299	300	301	302	303	304	305	306	307	308	309	310	311	312	313	314	315	316	317	318	319	320	321	322	323	324	325	326	327	328	329	330	331	332	333	334	335	336	337	338	339	340	341	342	343	344	345	346	347	348	349	350	351	352	353	354	355	356	357	358	359	360	361	362	363	364	365	366	367	368	369	370	371	372	373	374	375	376	377	378	379	380	381	382	383	384	385	386	387	388	389	390	391	392	393	394	395	396	397	398	399	400	401	402	403	404	405	406	407	408	409	410	411	412	413	414	415	416	417	418	419	420	421	422	423	424	425	426	427	428	429	430	431	432	433	434	435	436	437	438	439	440	441	442	443	444	445	446	447	448	449	450	451	452	453	454	455	456	457	458	459	460	461	462	463	464	465	466	467	468	469	470	471	472	473	474	475	476	477	478	479	480	481	482	483	484	485	486	487	488	489	490	491	492	493	494	495	496	497	498	499	500	501	502	503	504	505	506	507	508	509	510	511	512	513	514	515	516	517	518	519	520	521	522	523	52
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	Type	L #	Hits	Search Text	DBs	Time Stamp	Comment	Error	Definition	Errors
1	BRS	L1	11136	(pulse time) adj5 (width near8 detect\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/1 5 12:44				
2	BRS	L2	23424	(phase near8 shift\$4) 9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/1 5 12:45				
3	BRS	L3	1064	1 and 2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/1 5 12:45				
4	BRS	L4	17402	(count\$3 near8 (frequency adj5 divid	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/1 5 12:46				
5	BRS	L5	106	3 and 4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/1 5 12:46				

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# Ready

- L1: (11136) (pulse time) adj5 (width near8 detect\$4)
- L2: (234249) (phase near8 shift\$4)
- L3: (1064) 1 and 2
- L4: (17402) (count\$3 near8 (frequency adj5 divid\$3))
- L5: (106) 3 and 4
- L6: (125826) (pll (phase adj5 loop))
- L7: (1239) 1 and 6
- L8: (127) 4 and 7
- L9: (82) 2 and 8
- L10: (317) 6 same 1
- L11: (20) 4 and 10

Search List Browse Update Clear

DBs: US-PGPUB, USPAT, USOCR, FPRS, EPO, JPO, DERWENT, IBM, TDB

Default operator: OR

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4 and 10

☒ BRS form
 ☒ IS&R form
 ☒ Image
 ☒ Text
 ☒ HTML

	U	Document ID	Issue Dat	Page	Title	Current OR	Current XR	Retrieval	Inventor	S	C	P	3		
5	<input checked="" type="checkbox"/>	US 7180377	20070220	15	Method and apparatus for a hybrid phase lock	331/17	327/157;		Leong; Colin Wai	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	B1			for a hybrid phase lock		331/DIG.2		Mun et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
6	<input checked="" type="checkbox"/>	US 7177611	20070213	16	Hybrid control of phase locked loops	455/260	331/17;		Goldman; Stanley	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	B2			locked loops		375/376;		Jav	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
7	<input checked="" type="checkbox"/>	US 7084709	20060801	15	Hybrid analog/digital phase lock loop frequen	331/11	327/156;		Leong; Colin Wai	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	B1			phase lock loop frequen		327/157;		Mun et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
8	<input checked="" type="checkbox"/>	US 6580775	20030617	6	Method of detecting frequency of digital ph	375/375	327/151;		Park; Hyun-Soo et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	B1			frequency of digital ph		327/162;			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
9	<input checked="" type="checkbox"/>	US 6518845	20030211	13	PLL frequency synthesizer circuit	331/16	331/DIG.2		Nakamichi; Hiroto	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	B2			synthesizer circuit					<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
10	<input checked="" type="checkbox"/>	US 6441691	20020827	16	PLL cycle slip compensation	331/25	327/12;		Jones; Theron et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	B1			compensation		331/156			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
11	<input checked="" type="checkbox"/>	US 6392641	20020521	23	PLL circuit for digital display apparatus	345/213	331/1A;		Nishimura; Eizo et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
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12	<input checked="" type="checkbox"/>	US 5783972	19980721	14	Power saving PLL circuit	331/17	327/157;		Nishikawa; Masato	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A			circuit		327/159;			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
13	<input checked="" type="checkbox"/>	US 5661425	19970826	24	Digital PLL circuit	327/159	327/147;		Minoda; Hidenori et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A			Digital PLL circuit		327/150;			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
14	<input checked="" type="checkbox"/>	US 5548249	19960820	34	Clock generator and method for generating a	331/1A	327/107;		Sumita; Masaya et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A			method for generating a		327/150;			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
15	<input checked="" type="checkbox"/>	US 4996596	19910226	28	Phase synchronizing circuit in video signal	348/542	348/E5.021		Hirao; Yoshichika et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A			circuit in video signal					<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
16	<input checked="" type="checkbox"/>	US 4727530	19880223	29	Disc rotation control device for a disc plave	369/47.45	369/111;		Tomisawa; Norio	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A			device for a disc plave		386/86			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
17	<input checked="" type="checkbox"/>	US 4707856	19871117	12	AM stereo receiver	381/15	324/76.47;		Tanaka; Kanji et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A			AM stereo receiver		324/76.55			<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
18	<input checked="" type="checkbox"/>	US 4688246	19870818	14	CATV scrambling system with compressed digital	380/237	348/E7.028		Eilers; Carl G. et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
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19	<input checked="" type="checkbox"/>	US 3868677	19750225	12	PHASE-LOCKED VOLTAGE-TO-DIGITAL CONV	341/157	341/158		Kidd; Marshall C.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
	<input checked="" type="checkbox"/>	A			PHASE-LOCKED					<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
20	<input checked="" type="checkbox"/>	GB 2194714	19880309	11	Phase locked loop for ISDN in telephone syste				ERKKU, J H et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
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